

Academic Calendar for 2025-26 Admitted Batch
M.Tech -Master of Technology (VLSI Design and Embedded Systems/CSE/Data Science)

III Semester	Date(s)
Classwork Commencement	24-07-2026
Course Work Registration	24-07-2026 to 27-07-2026
Last Date for Fee Payment	24-07-2026
Finalization of Project Title/ Internship (0th Review)	29-08-2026
First Continuous Assessment (1st Review)	31-10-2026
Submission of MOOC Certificate	06-11-2026
Submission of the Final List of Eligible Candidates	16-11-2026
Last working Day	17-11-2026
Generation of Hall Ticket	19-11-2026
Evaluation of Dissertation-I/ Industrial Project Phase-I/Internship	23-11-2026 to 24-11-2026
Semester End Examination(MOOC)	25-11-2026 to 26-11-2026
Posting of Continuous Internal Assessment (CIA) and Dissertation-I/ Project Work Phase-I/ Internship Final Assessment Marks	28-11-2026
Semester Break	29-11-2026 to 30-12-2026
Publication of III Semester Result	15-12-2026

IV Semester	Date(s)
Commencement of Project Work	01-12-2026
Course Work Registration	01-12-2026 to 03-12-2026
Last Date for Fees Payment	01-12-2026
Second Continuous Assessment (2nd Review)	23-01-2027
Third Continuous Assessment (3rd Review)	20-03-2027
Last Date for Completion of Project / Internship Work	31-03-2027
Submission of the Final List of Eligible Candidates	01-04-2027
Submission of Project Report/ Internship Last Working Day	03-04-2027
Final Viva Voce/ Evaluation of Dissertation-II/ Industrial Project Phase-II/ Internship	05-04-2027to 07-04-2027
Posting of Continuous Internal Assessment (CIA) and Dissertation-II/ Project Work Phase-II/ Internship Final Assessment Marks	15-04-2027
Publication of IV Semester Result	28-04-2027

Anil Kumar. S
18/6/26

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